

Hardware Engineer, Entry Level

March 2019

At Roswell, we are on mission to make genome sequencing dramatically cheaper and faster--now, not in the distant future. Our technology is powered by molecular electronics, and we are the global pioneers in developing this new field that integrates CMOS chips, nanotechnology, and biochemistry. The leadership of Roswell have unmatched experience in developing high-impact, high-value, high-tech devices for the Life Sciences, and bringing them to market, globally. Our goals are big, our timelines are short, and we are building the team that will make it happen.

The **Entry Level Hardware Engineer** will be responsible for testing the Roswell CMOS integrated circuit (IC) sensor chips, designing printed circuit boards (PCBs), debugging circuits with the team, and carrying out test plans. The Engineer will work under the supervision of the senior hardware engineers and senior IC designers, as part of the Chip and Instrument Engineering Teams. Primary responsibilities consist of working closely with the IC design team to develop test protocols, creating the required test-benches, and performing the electronic measurements for new generations of sensor chips.

Specific responsibilities include:

- Developing IC test protocols, in consultation with the IC chip design team
- Performing design, layout, and assembly of PCB boards
- Performing test protocols, collection, analysis, and presentation of test data
- Support deployment of sensor ICs post testing, in conjunction with Instrument Team

The ideal candidate can thrive in an extremely fast-paced, dynamic environment, as part of a team of highly skilled, highly focused and highly motivated people who believe in changing the world by creating disruptive technology.

Qualifications: New and recent graduates are encouraged to apply. BSEE or MSEE, with at least one year experience in hardware test engineering in an academic or industry setting. Experience in IC testing is highly desirable. Knowledge of general laboratory test equipment is essential. Experience designing, laying out, and assembling PCBs is required, preferably using Altium. Experience debugging analog circuits is required. Experience working with LabVIEW is preferred. Experience working with FPGA systems is preferred. The abilities to work quickly, communicate easily, and a passion for building and testing hardware are essential for success.

U.S. Citizens, Green Card Holders, and those authorized to work in the U.S. for any employer will be considered. The position is located in the Sorrento Valley area of San Diego.